

# JOB PROFILE

## Senior Analog IC Design Engineer (location Enschede)

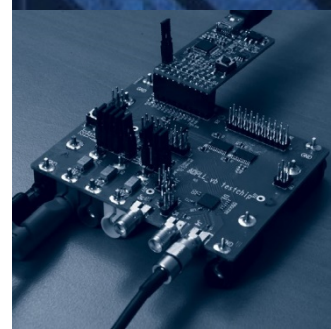
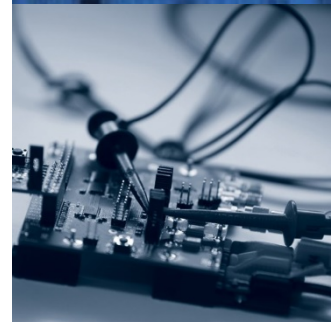
### Job description

A Senior Analog IC design engineer at ItoM is responsible for providing design solutions for application specific, mainly mixed-signal, IP blocks. Responsibilities include:

- Working within ItoM's multidisciplinary team to define the requirements for Analog & Mixed-signal IP blocks/systems based on overall requirements
- Understand trade-offs, as well as closely cooperate with RF, digital and software designers for optimum partition between domains
- Design Analog & Mixed-signal circuit blocks/systems such as Analog PLL, All Digital PLL, ADC, DAC, Filter, Regulator, Bandgaps, LDOs, Oscillators, etc.
- Conduct feasibility studies for new circuits and architectures, from analytical to transistor-level
- Floorplan and work with layout designer to implement circuit design with best-practice layout techniques
- Conduct reviews on analog designs, evaluation and test results
- Conduct analog circuit modelling, preferably with insights into packaging effects, PCB effects, supply isolations, high frequency ESD structures, and circuit layout for performance and manufacturability
- Define and execute test and validation plans

### Your profile and skills

- Experience with designing in several IC technologies
- Familiar with Cadence Virtuoso, Spectre (RF), Verilog-A, Matlab-Simulink, and similar tools
- Knowledge about IC technologies and basic understanding of device physics
- Excellent analytical and problem solving skills
- Self-motivated, efficient professional able to work alone or in teams
- Offer quality and reliability to colleagues, customers and management, willing to share knowledge and able to communicate efficiently
- Enthusiastic about further developing multidisciplinary experience from IC design to tested production chips
- Fluent in English, both written and spoken



## Skills specific to Senior engineer

A senior engineer has extended technical and/or non-technical responsibilities, not only on an individual level, but also for projects and the company. Such extended responsibilities can include:

- Higher technical and/or non-technical complexity
- Higher quality and/or lower design time
- Higher independence
- More extended involvement in an earlier phase of project definition
- Help, guide and/or mentor other engineers at (aspects of) their work
- More proactive and professional contribution to internal and external communication. This can include working towards new projects with potential customers or partners
- Improve the ItoM processes, contributing to the company development
- A senior engineer can be assigned the role of lead engineer for a (sub-) project with multiple engineers. The lead engineer is responsible for overall quality and time

## Benefits

- Work within an inspiring and highly experienced multi-disciplinary design team
- An attractive wage package with several extra benefits
- Participate in exciting breakthrough innovative IC projects for top 20 wireless semiconductor companies
- Broad activities, including system design, with a lot of responsibilities within a flexible and growing team

We look forward to receiving your resume.

Please send it to [recruitment@itom.nl](mailto:recruitment@itom.nl)

or contact Leonie Fokkens at +31 64 372 19 16.

Learn more about ItoM: [www.itom.nl](http://www.itom.nl)



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